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To:

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Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Date: December 7, 2005

Number of Pages: Cover + 16

FAX NO.:

1 (571) 273-8300

Examiner: DIEP, Nhon T.

TEL NO.: 1 (571) 272- 7328

From:

Micah P. Goldsmith, Esq.

Reg. No. 43,638

Re:

U.S. Patent Application Serial No.:

09/974,530

Filing Date: October 10, 2001 Title: MULTISTANDARD VIDEO DECODER AND DECOMPRESSION

SYSTEM FOR PROCESSING **ENCODED BIT STREAMS**

INCLUDING A VIDEO FORMATTER

AND METHODS RELATING

THERETO

Confirmation No.: 1318

Art Unit: 2613

Inventor: Adrian P. Wise et al.

Attorney Docket No.:

94100423(EP)USC1X1C1D7D1 PDDD

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TEL: (949) 660-5000

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Dear Examiner Diep:

Please find enclosed form PTO-1449 (IDS) filed 10/10/2001 (15 pages) you requested on November 29, 2005.

Kind regards,

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PATENT

94100423(EP)USC1X1C1D7D1 PDDD

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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In re the Application of: Adrian P. Wise

Filed: October 10, 2001

MULTISTANDARD VIDEO DECODER AND DECOMPRESSION SYSTEM FOR PROCESSING ENCODED BIT STREAMS INCLUDING A § VIDEO FORMATTER AND METHODS RELATING THERETO

Application No.: Not Yet Known

Art Unit: 2783 (anticipated)

Examiner: Follansbee, J. (anticipated)

Attorney Docket No.

94100423(EP)USC1X1C1D7D1PDDD

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. § 1.97

BOX PATENT APPLICATION Assistant Commissioner of Patents Washington DC 20231

Sir:

FILING OF CORRESPONDENCE BY EXPRESS MAIL UNDER 37 C.F.R. > 1.10 EL443264408US October 10, 2001

In complying with the duty of disclosure set forth in 37 CFR § 1.56, Applicant submits lists of patents and publications (these were previously submitted in 09/307,239) as listed on the attached form PTO-1449, and a concise explanation of the relevance of each listed item not in the English language as follows:

No non-English language references are submitted.

The Commissioner is hereby authorized to charge payment of the fee required by 37 C.F.R. § 1.17(i), 37 C.F.R. § 1.17(p), and any fees associated with this communication or credit any overpayment to Deposit Account No. 04-1175.

Respectfully submitted.

Date: 10/10/01

DISCOVISION ASSOCIATES

DISCOVISION ASSOCIATES INTELLECTUAL PROPERTY DEVELOPMENT P.O. Box 19616 Irvine, California 92623 (949) 660-5006

Richard Stokev Reg. No. 40,383

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ATTY, DOCKET NO. 941004(EP)USC1X

SERIAL NO.

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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(Use several sheets if necessary)

APPLICANT ADRIAN P. WISE ET AL

FILING DATE

MARCH 7, 1995

GROUP 2783

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U.S. PATENT DOCUMENTS

EXAMINER INITIAL		PATENT NUMBER	ISSUE DATE	PATENTEE	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
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	AB	4225920	C5/30/80	STOKES	364	200	09/11/78
	AC	4228497	10/14/80	GUPTA ET AL	364	200	11/17/77
74	AD,	4307447	12/22/81	PROVANZANO ET	364	200	05/19/79
46	AE	4467409	08/21/84	POTASH ET AL	364	200	G8/05/80
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74	AG	4710866	12/01/87	ZOLNOWSKY ET AL	364	200	10/07/86
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74	AI	4811214	03/07/89	NOSENCHUCK ET AL	364	200	11/14/86
_ 46_	AJ	4811413	03/07/89	KIMMEL	332	41	10/22/87
JH_	AK	4841435	06/20/89	ASANO ET AL	364	200	05/30/86
74	AL	4855947	08/08/39	ZANSLOWSKI ET	364	200	05/27/\$7
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44	AQ	5184347	02/02/93	Farwell et al.	370	94.1	07/09/91
744	AR	5203003	04/13/93	DONNER	395	800	03/28/91
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ADRIAN P. WISE ET AL

FILING DATE

MARCH 7, 1995

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FOREIGN PATENT OR PUBLISHED FOREIGN PATENT APPLICATION

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74	F01	0618723	05.10.94	E.P			
24	F02	9425935	11/10/94	wo	C-96%	+5/cc	NO

OTHER DOCUMENTS (Including Author, Title, Date**, Relevant Pages, Place of Publication***)

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74+	P09	YANG, KUN-MIN. VLSI ARCHITECTURE DESIGN OF A VERSATILE VARIABLE LENGTH DECODING CHIP FOR REAL- TIME VIDEO CODECS, TENCON 1990 IEEE REGION 10 CONFERENCE ON COMPUTER AND COMMUNICATION, IEEE PUBLICATIONS FEBRUARY 1990, PP 551-554,



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AT 17. DOCKET NO. P000 2941004(22)USC1C:P SERIAL NO.

09/3072 39

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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APPLICANT Wise et al.

FILING DATE 03/07/95

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FILING DATE 03/07/95

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Wise at al.

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46	AR	4	3	3	1	2	1	6	6/8/82	Saran	358	261	5/15/80
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74C	AU	4	4	9	5	ŝ	2	9	1/22/85	Zasio et al.	377	70	1/25/93
74	AV	4	5	4	0	3	0	3	9/10/85	Cooke et al.	307	465	10/17/83

FOREIGN PATENTS OR PUBLISHED FOREIGN PATENT APPLICATIONS

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APPLICANT Wise ot al.

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(37 CFR 1.98(b)

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- 244	CG	4	9	2	2	3	4	1	5/1/90	Strobach	358	136	9/26/88
74	СН	4	9	2	1	2	9	8	5/8/90	Kitamura	358	12	9/15/83
- 7th	CI	4	9	2	4	3	9	а	5/9/90	Fauchtwanger	358	133	3/7/89
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74	EC	5	1	7	2	0	1	1	12/15/92	Lauthold at al.	307	272.2	6/30/89
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24	ΕĒ	5	1	7	9	3	7	2.	1/12/93	West et al.	340	799	3/29/91
744	٤۴	5	1	3	2	5	4	2	1/26/93	Gersdorff at al.	358	133	4/19/91
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74	EW	5	2	2	3	9	2	5	6/29/93	Stone et al.	358	133	12/19/91
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74	FC	5	2	3	3	4	2	Ģ	3/3/93	Piri et al.	358	149	4/10/85
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74	FF	5	2	4	1	2	2	2	8/31/93	Small et al.	307	449	12/20/91
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74	AO	Komori et al., "An Elastic Pipelline Machanism By Self-Timed Circuits, IEEE Journal Of Solid-State Circuits, Vol. 23, No. 1, 2/88, New York, NY, USA, pp. 111-117
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